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(54) An image defect correcting circuit for a solid state imager.

(57) An image defect correcting circuit for a solid state imager is arranged such that levels of output signals of pixels of a solid state image pickup element (51R, 51G, 51B) illuminated on its whole surface with a light of uniform brightness are compared with a predetermined level, data indicative of a position of a pixel which outputs a signal of peculiar level is generated on the basis of the compared result, a video signal output from at least the pixel located at the position indicated by the position data is interpolated by using output signals of other pixels according to an interpolation processing (63) and the output signals of the respective pixels and the video signal generated by the interpolation processing are selectively output on the basis of the position data.

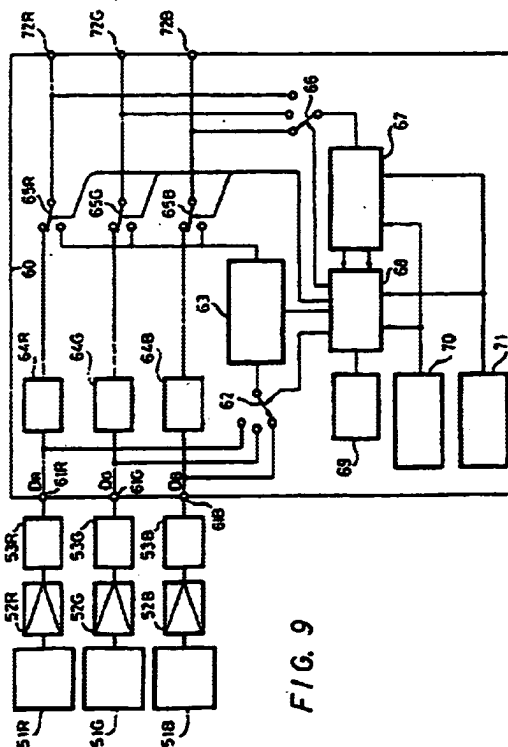


FIG. 9

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Jouve, 18, rue Saint-Denis, 75001 PARIS



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The present search report has been drawn up for all claims				
Place of search THE HAGUE		Date of completion of the search 10 FEBRUARY 1992	Examiner DIJMR R.H.J.E.	
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